

FIG. 1

FIG. 3

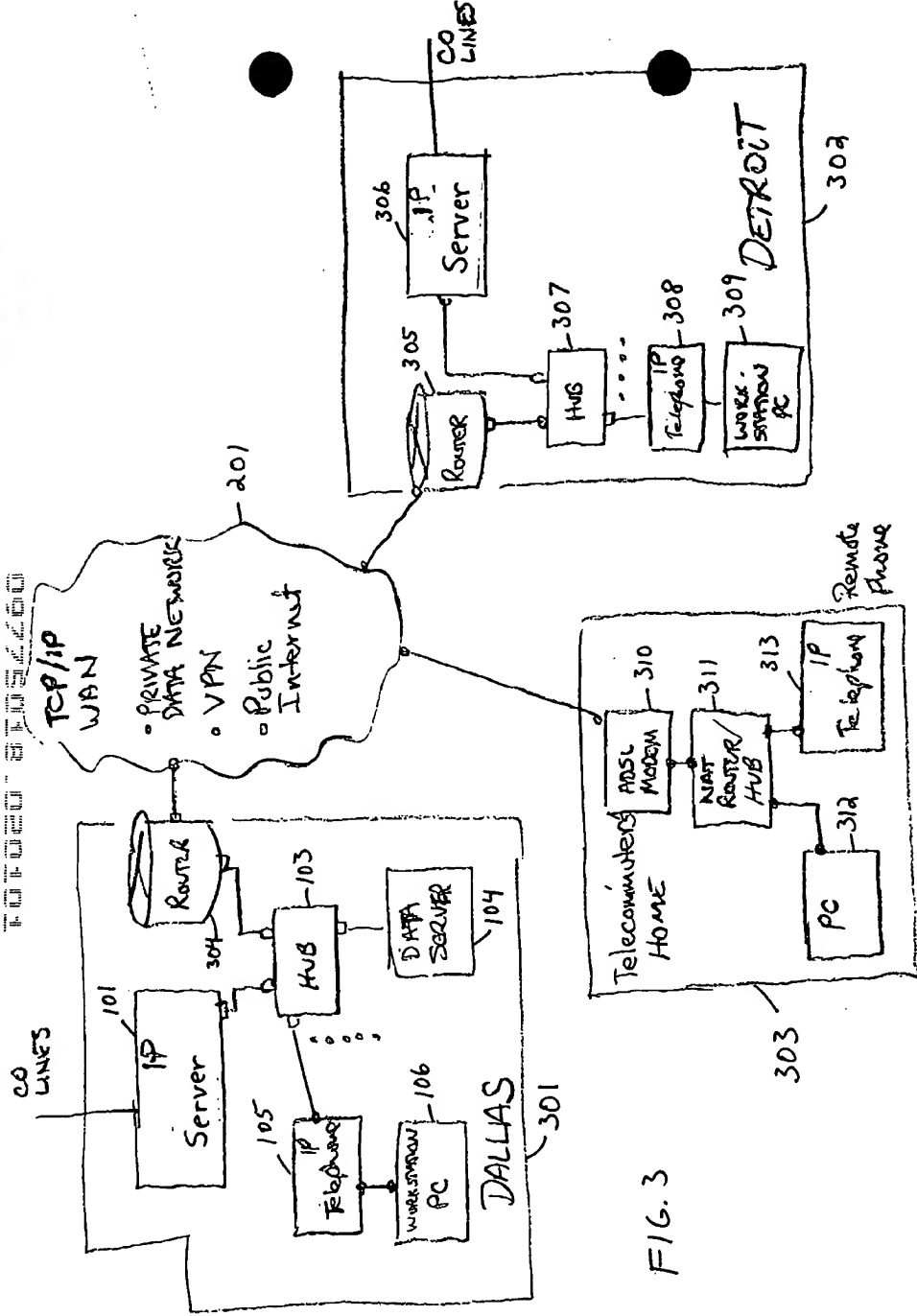
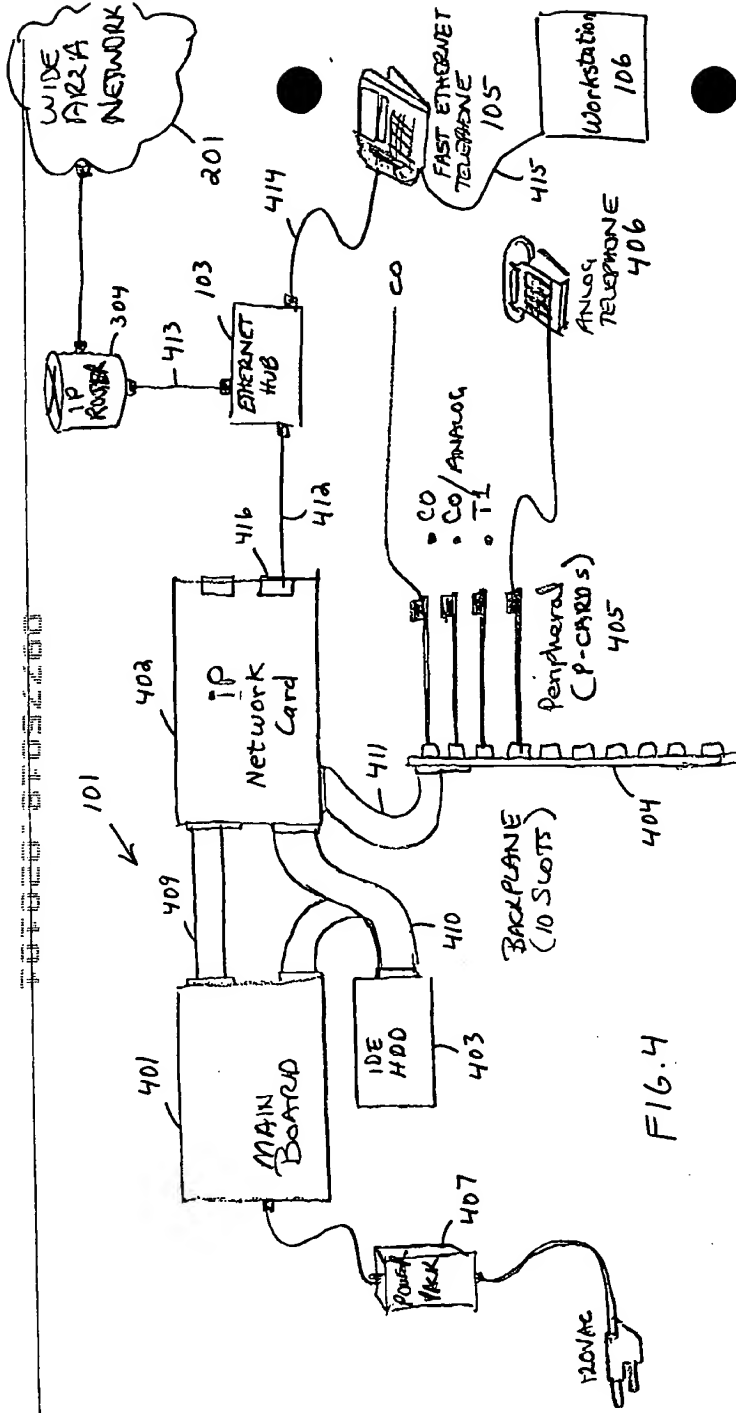


FIG. 3



DSP FARM EXPANSION CONNECTOR

402

501

410

DE HDD  
CONNECTION  
FROM M.B.

503

EEP

DSP

DSP

510

508

506

DIGITAL  
CROSSPOINT  
SWITCH

DRAM

FIG. 5

SRAM

511

416

10/100  
MAC/PHY

516

513

514

10/100  
MAC/PHY

515

517

CONNECTORS  
BACKPLANE PCARD PCARD L  
405 405 405

404

I/O  
AND  
BUFFERS

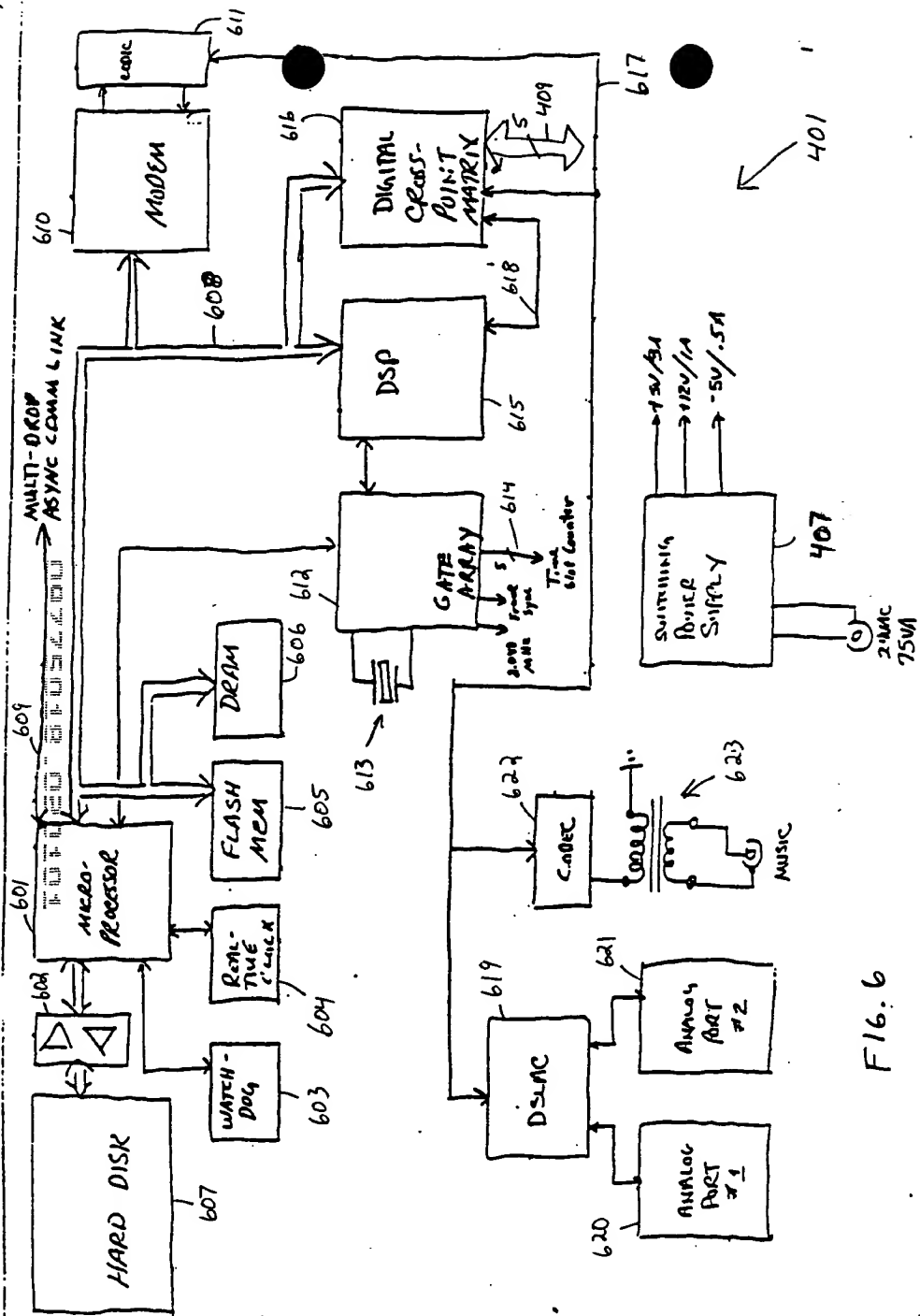
512

SYSTEM  
HIGHWAYS

409

509

411



F16.6

FIG. 6 OF 6

405

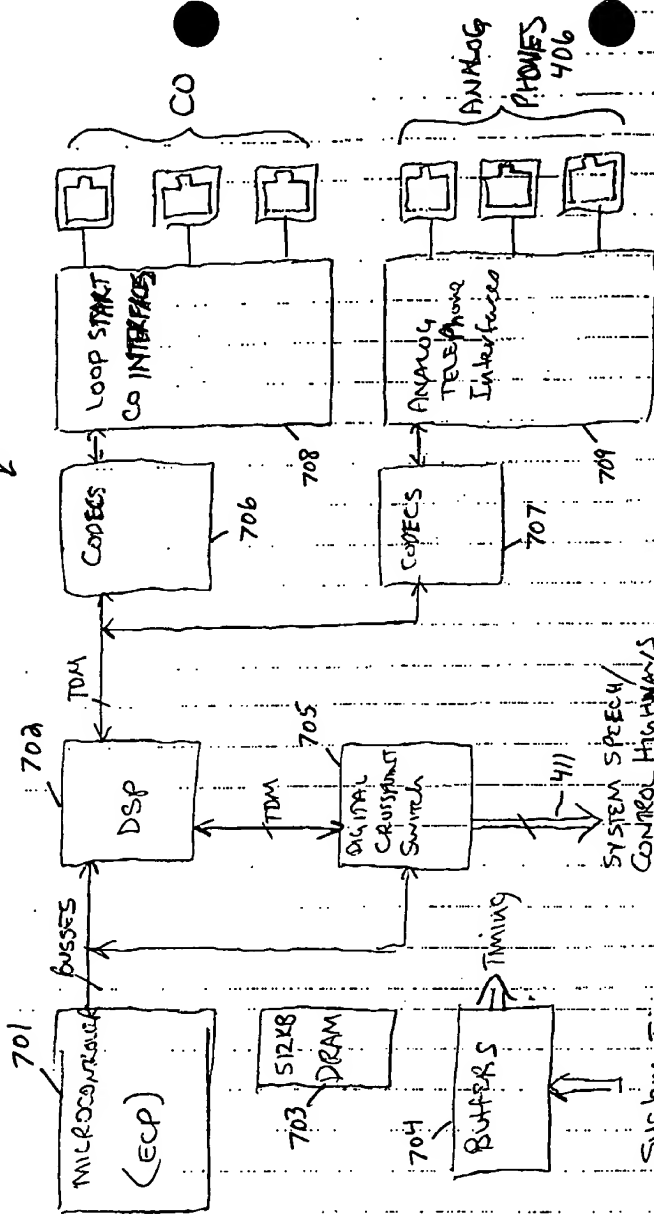


Fig. 7

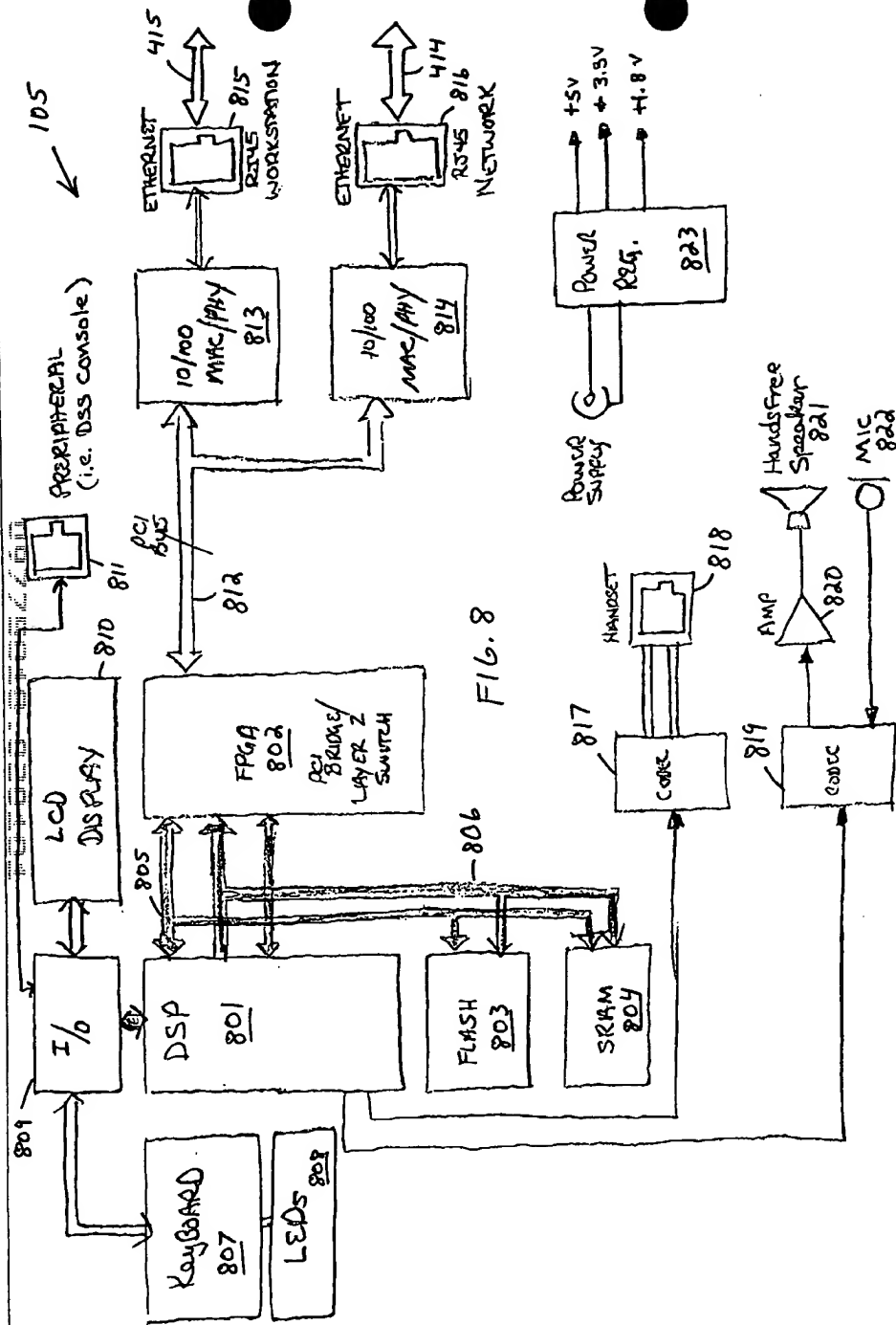
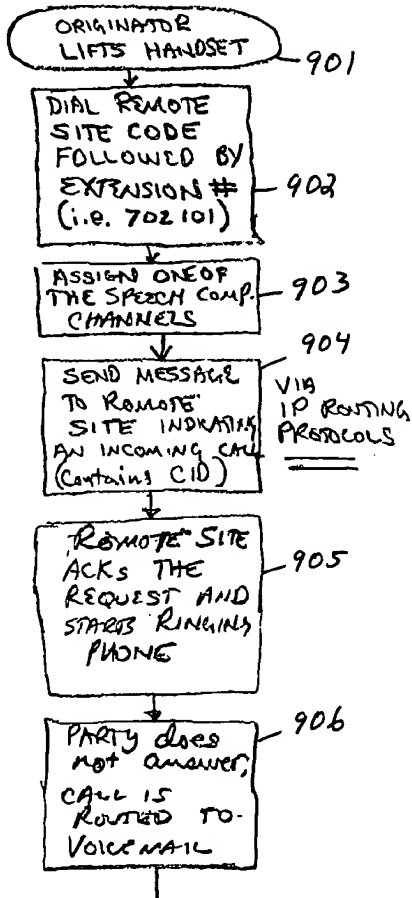


FIG. 8

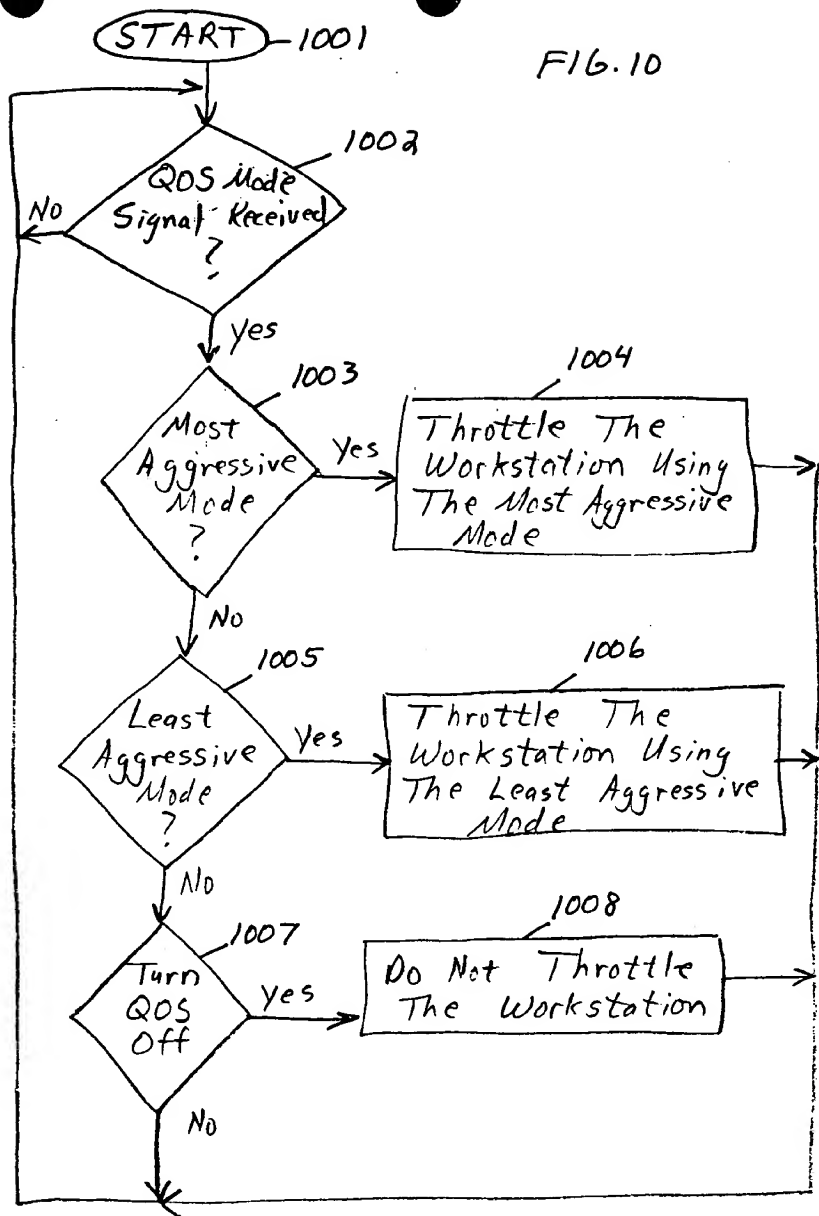
FIG. 9



0075018-00101



FOR RELEASE



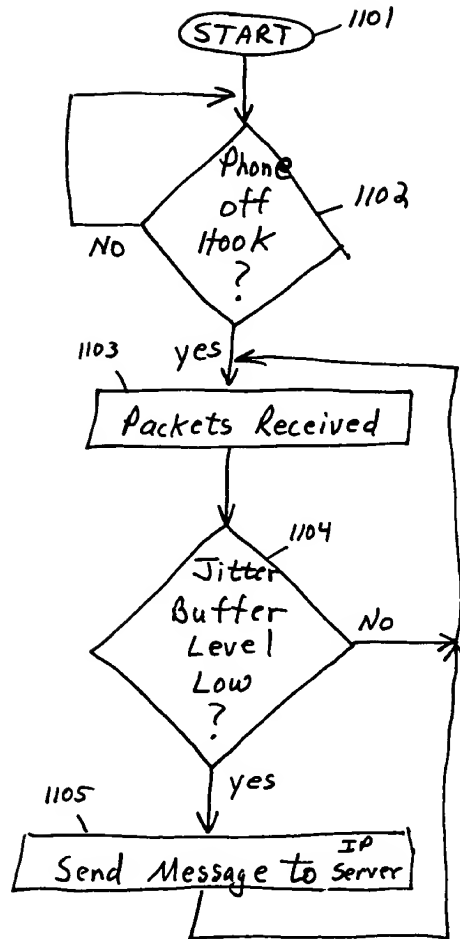
[illegible]

FIG. 11

FIG. 12A

